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4. (Twice Amended) The chip carrier according to claim 1 wherein the base includes an upper surface and wherein the outer well has a first base and the inner well has a second base wherein there is a first distance between the first base and the upper surface and there is a second distance between the second base and the upper surface, the first distance greater than the second distance.

## (3) Please rewrite Claim 25 as follows:

25. (Amended) A chip carrier comprising:

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a base;

an inner well extending along a length of a periphery of the base; and an outer well extending along a length of a periphery of the inner well, the chip carrier adapted to temporarily hold an integrated circuit in the chip carrier.

## **REMARKS/ARGUMENTS**

The Applicants have carefully considered this application in connection with the Examiner's Action and respectfully request reconsideration of this application in view of the foregoing amendment and the following remarks.

The Applicants originally submitted Claims 1-20 in the application. Pursuant to a restriction requirement, the Applicants subsequently canceled Claims 8-20 without prejudice or disclaimer. The Applicants also previously added Claims 21-28. The Applicants presently amend Claims 1, 4 and 25. Support for these amendments may be found in FIGUREs 1 and 2 of the present application, which clearly indicate that the inner and outer wells are different from a plurality of